

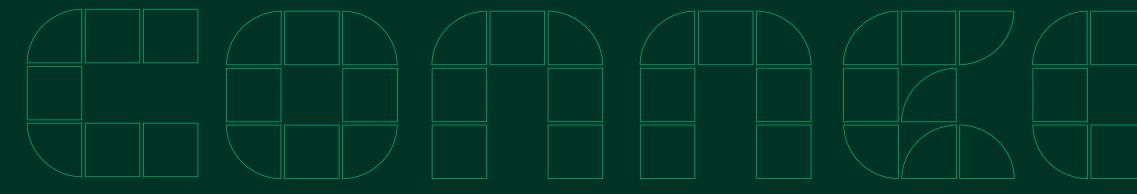
# **Malleable VI's unlocked Natan Biesmans**



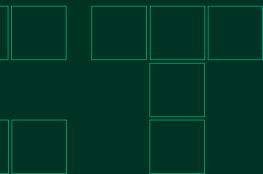




#### 



#### Introduction





# **Natan Biesmans**





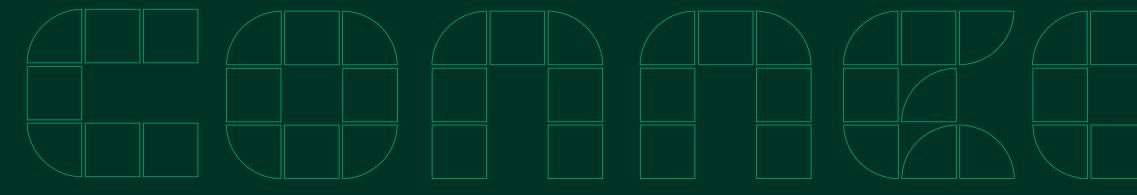


- CLA, LabVIEW Champion
- Started using LabVIEW 10 years ago. \_
- Previous presentations \_

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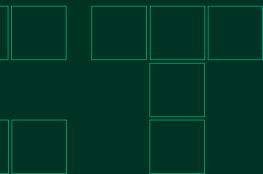
- FPGA Accelerated Computing
- Native LabVIEW Applications on Android and IOS
- GPU computing made easy with G<sup>2</sup>CPU

- Architect of G<sup>2</sup>CPU, the CPU and GPU HPC compute toolkit for LabVIEW.
- QPLOX Engineering: LabVIEW R&D Architect @ IMEC —
- National Instruments (6 years ago): Application Engineer.



#### Introduction

What makes LabVIEW stand out



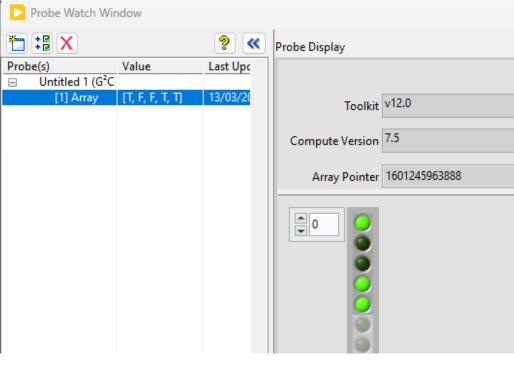


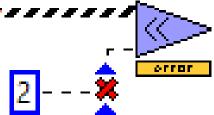
# What makes LabVIEW unique

- Fast development and verification
  - Compiled: directly know if your code has runtime errors.
  - Powerful debugging tools
    - Setting probes at runtime
    - Execution highlighting
    - Each function has a UI

"The tools of a scripting language, the speed of a compiled language"







	Natan's unnamed CPU-0
Platform	CUDA
Device Name	NVIDIA_GeForce_RTX_20
Average	0,6 Std dev

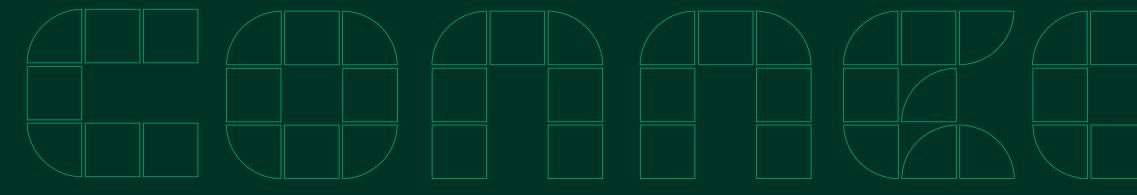
# What makes LabVIEW unique

- Hyper reuseable code
  - Across CPU's
  - Operating Systems

"Code it once and code it right"

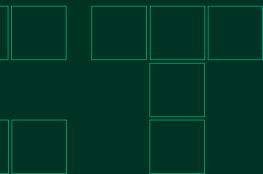
- Real-time
- FPGA
- Bitness

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#### Introduction

Origins of this presentation





- G<sup>2</sup>CPU the GPU and CPU HPC toolkit for LabVIEW
  - Hundreds of functions (not all on screen)
    - Data Management
    - Math
    - Linear Algebra
    - Statistics
    - Vector Algorithms
    - Boolean Logic
    - Signal Processing
    - Machine Vision
    - Image processing
    - File IO

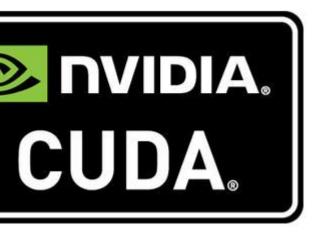
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- G<sup>2</sup>CPU the GPU and CPU HPC toolkit for LabVIEW
  - Hundreds of functions •
  - Several technologies •
    - CUDA
    - OpenCL
    - CPU
    - Intel OneAPI









- G<sup>2</sup>CPU the GPU and CPU HPC toolkit for LabVIEW
  - Hundreds of functions
  - Several technologies
  - Different environments
    - Windows
    - Linux
    - LabVIEW RT





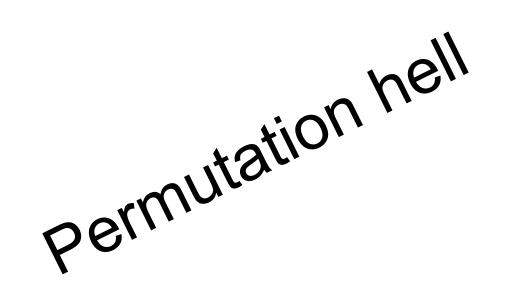




- G<sup>2</sup>CPU the GPU and CPU HPC toolkit for LabVIEW
  - Hundreds of functions •
  - Several technologies •
  - Different environments •
  - Lots of input variations •
    - 13 datatypes
    - 4 Dimensions
    - GPU and LabVIEW Arrays

Scalars

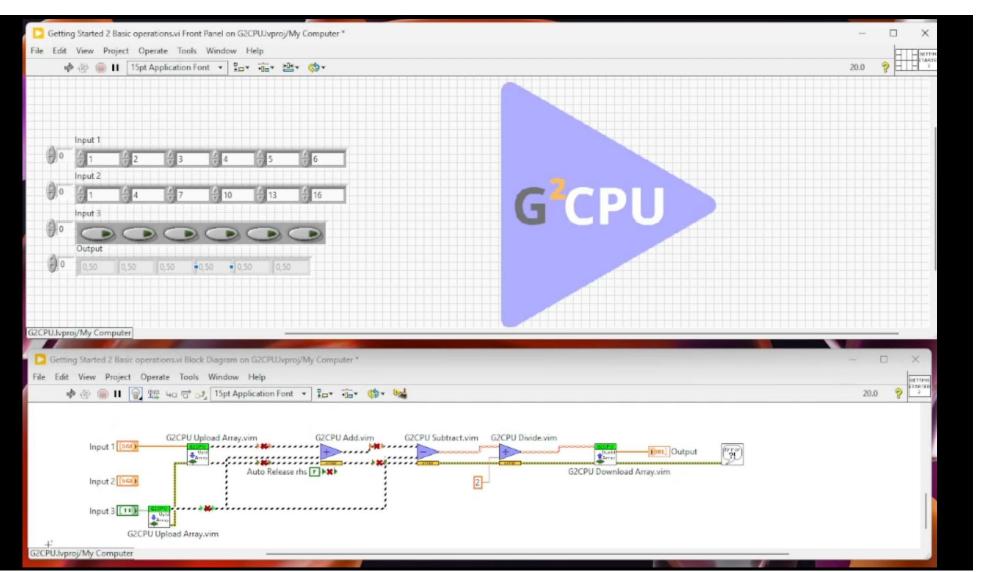
A simple addition has over 12 168 possible permutations

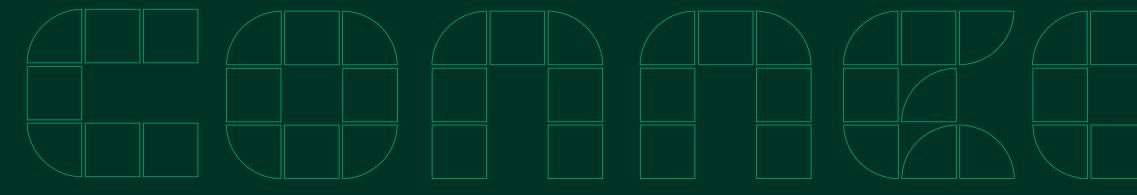




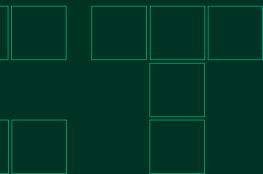
# Origins of this presentation API lessons learned

- Provide a near LabVIEW native experience
- Development time evaluation
- Dynamic wire appearance
- Selective probes
- Managing permutations
- Unit tests





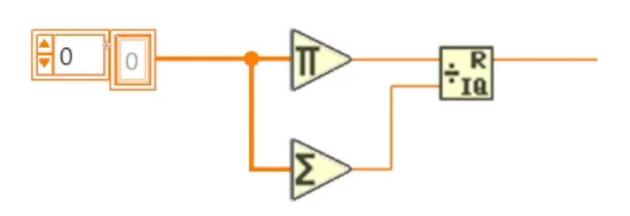
#### Why should you use Malleable VIs

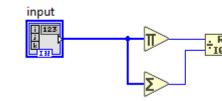




# What Are Malleable VIs

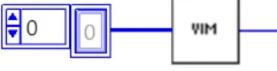
- Special VI type called VIM
- Technique for dynamic code execution
- Pass through of datatypes
- Development time evaluation





# What Are Malleable VIs

- Special VI type called VIM
- Technique for dynamic code execution
- Pass through of datatypes
- Development time evaluation





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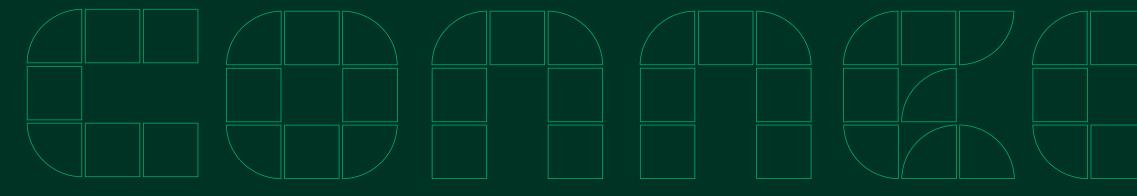
# How to make a malleable VI

- Convert VI
  - .vim extension
  - Inline SubVI
  - Preallocated Clone Reentrant Execution
  - No error handling
- Template

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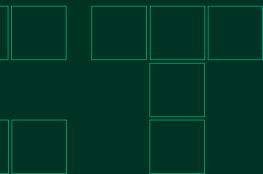
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#### Why should you use Malleable VIs

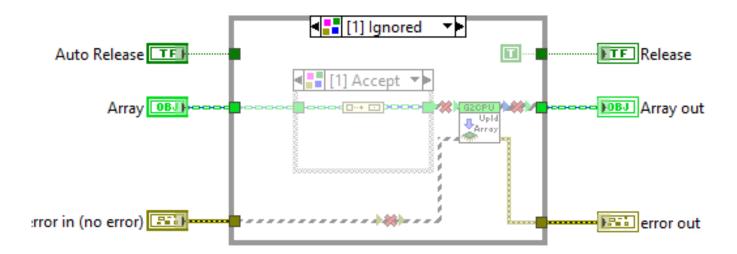
Fundamentals of scalable malleable VIs





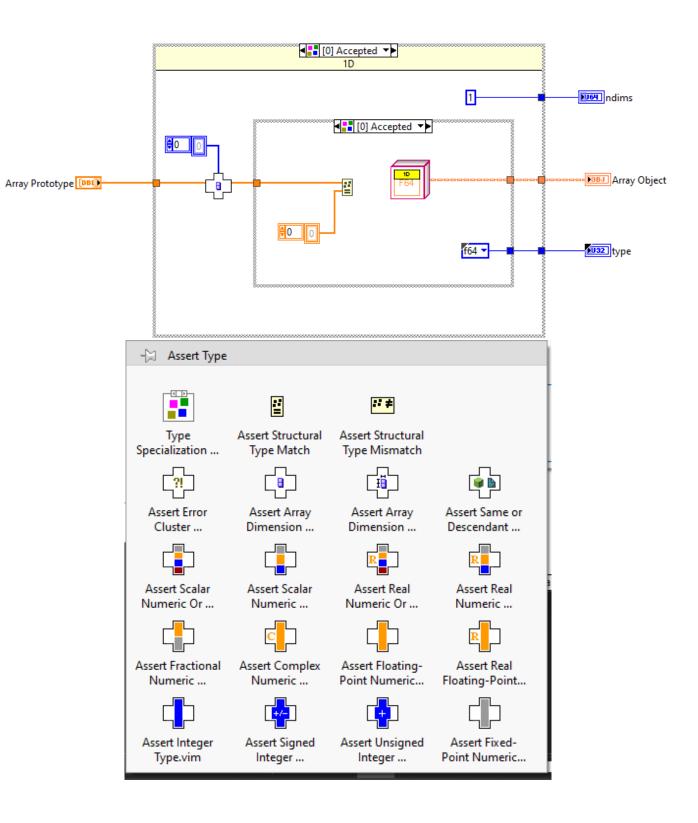
# **Type Specialisation**

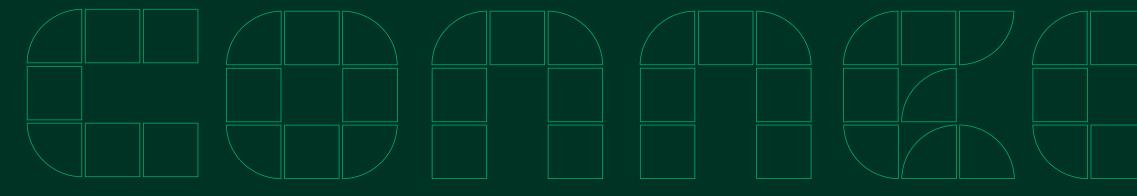
- Define multiple behaviors based on certain conditions.
- Evaluate each frame sequentially
  - If frame can be compiled it will be used
  - If a frame can't be compiled, go to next.
  - All other frames won't be evaluated



# Assert

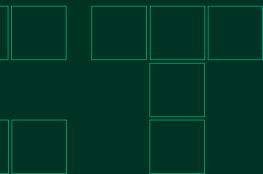
- Break code under certain conditions
- Used to
  - Only allow certain types
  - Disallow certain types



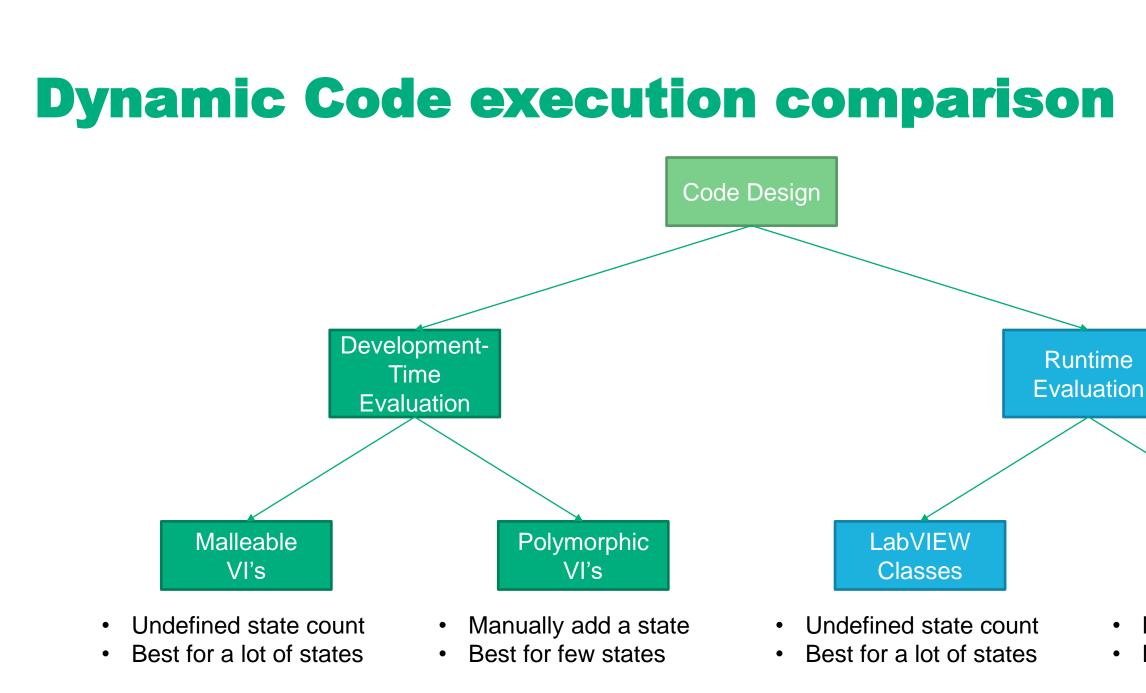


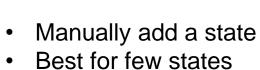
#### Why should you use Malleable VIs

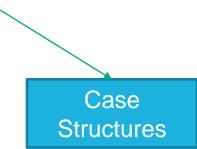
When should you use malleable VIs

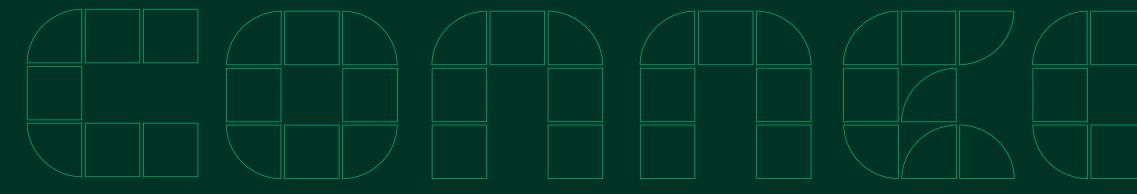






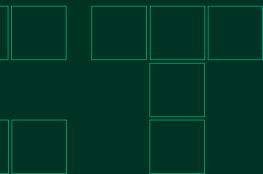






#### Advanced Techniques

LabVIEW Classes

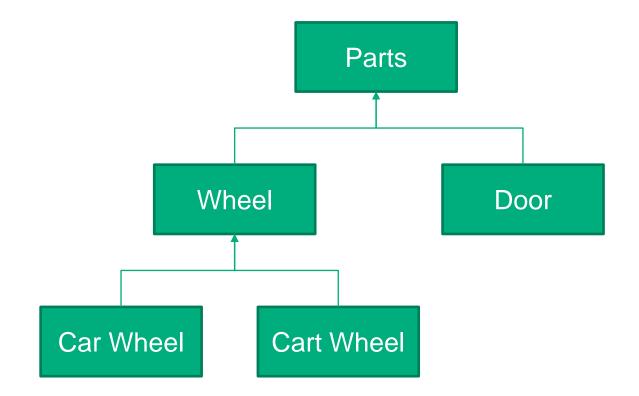




# **LabVIEW Classes**

Assign attributes to objects

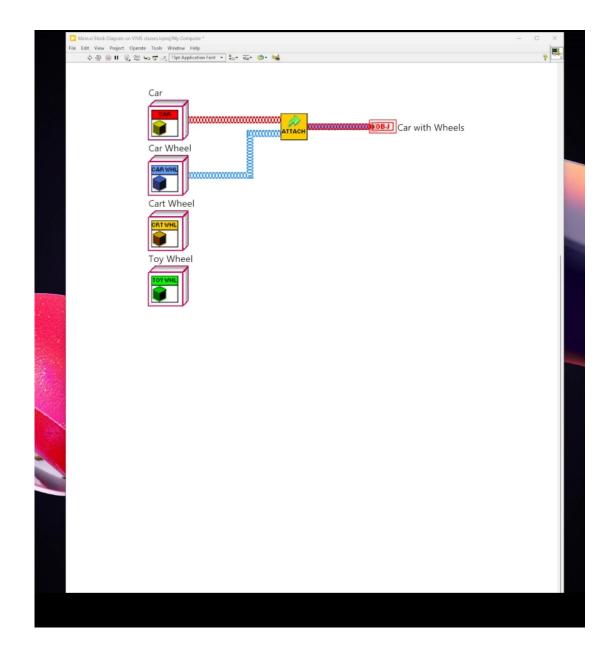
- IDE Decision making
- More flexibility than Dynamic Dispatch by using child classes
- Wire colors
- Dynamic probes

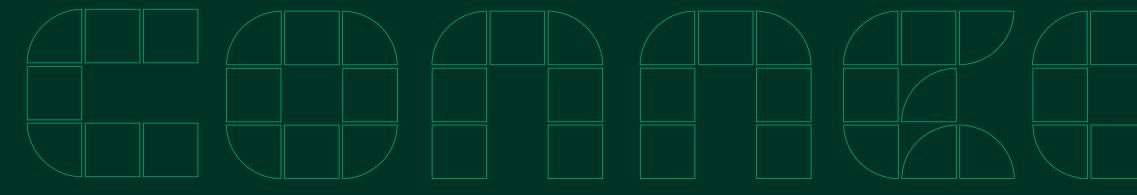


# **LabVIEW Classes**

Assign attributes to objects

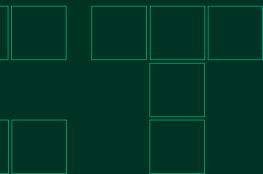
- IDE Decision making
- More flexibility than Dynamic Dispatch by using child classes
- Wire colors
- Dynamic probes





#### Advanced Techniques

LabVIEW Interface Classes





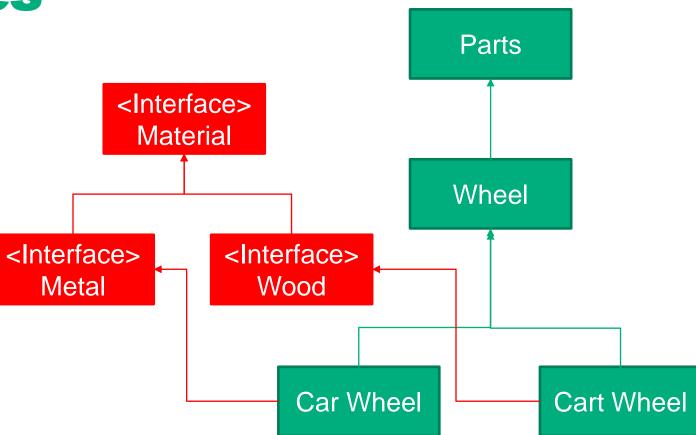
# **LabVIEW Interfaces**

Assign more attributes to objects

• More IDE Decision making fidelity

• Greater wire color control

• Wider range of dynamic probes



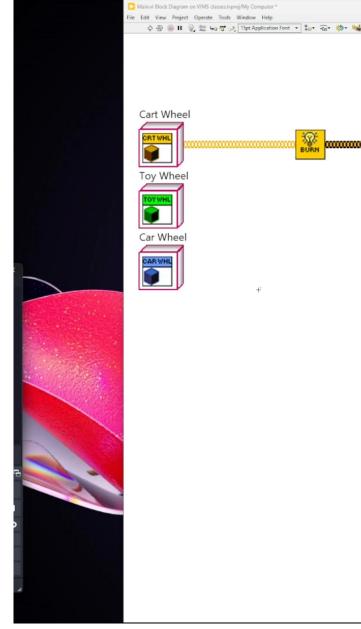
# **LabVIEW Interfaces**

Assign more attributes to objects

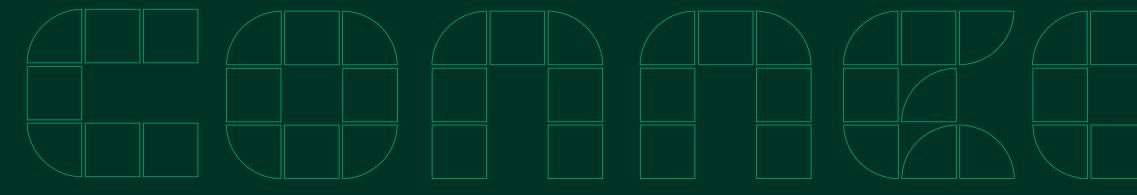
• More IDE Decision making fidelity

• Greater wire color control

 Wider range of dynamic probes by using Interface classes

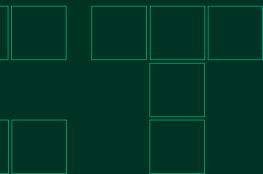


Burn Result



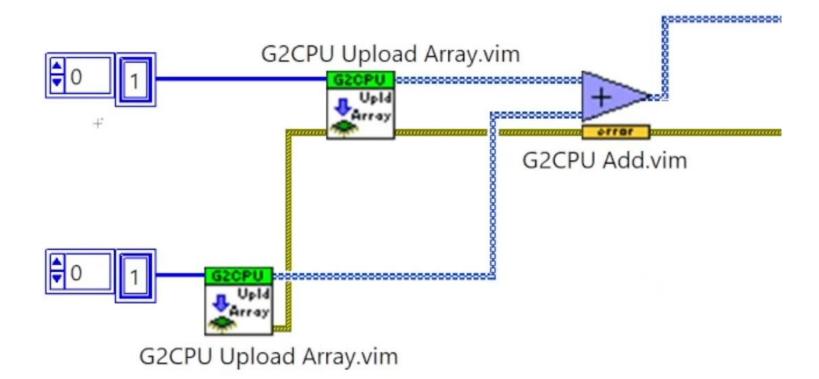
#### Advanced Techniques

LabVIEW Core Functions



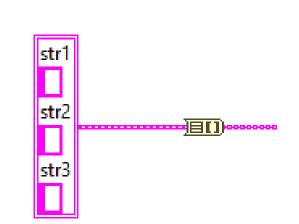


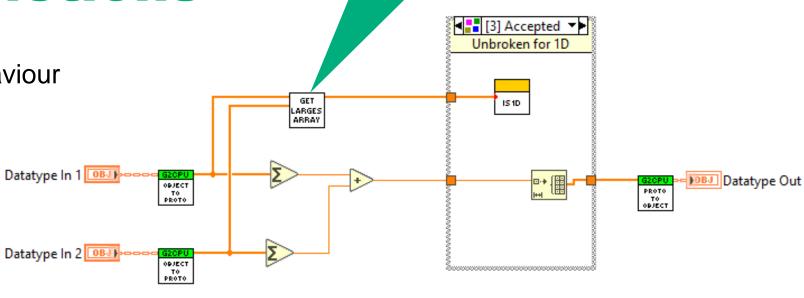
# **LabVIEW Core Functions**



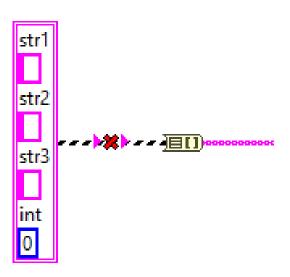
# **LabVIEW Core Functions**

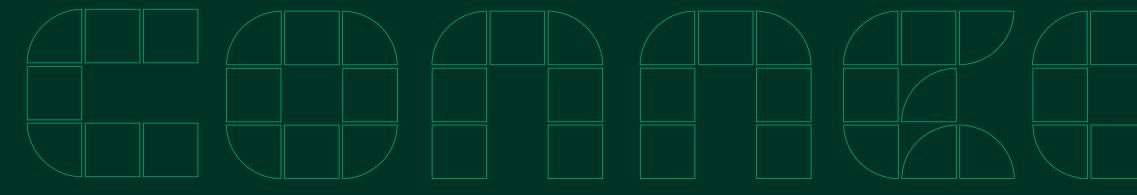
- Use LabVIEW functions for their IDE behaviour
  - Add
  - Add array elements
  - Initialise array
  - Cluster to array
- No runtime impact
  - Depends on compiler optimizations
  - No data going through the functions
  - Output values not used





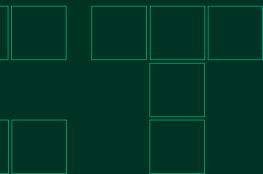
#### More on this VIM later





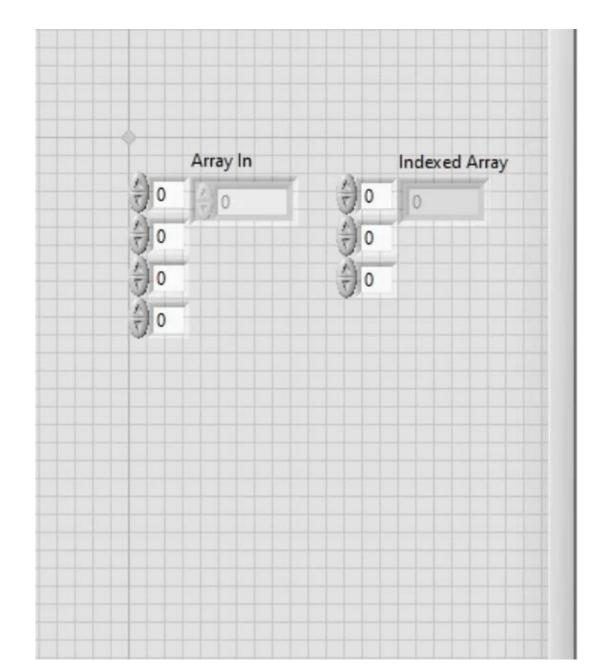
#### Advanced Techniques

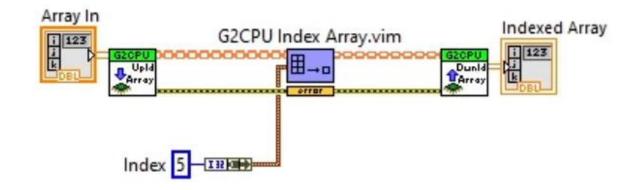
Maths





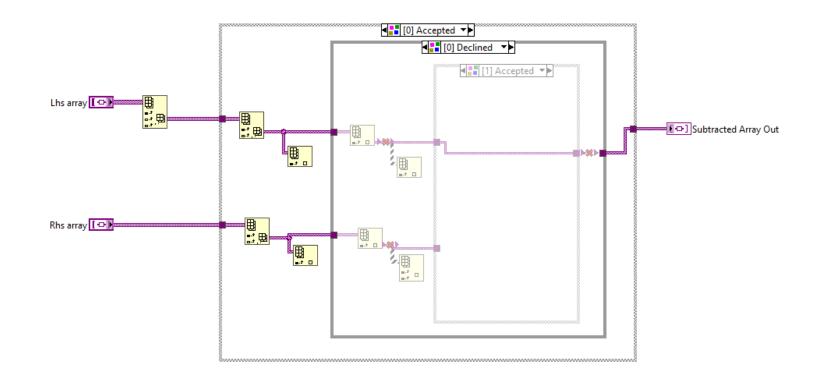
# **Maths**

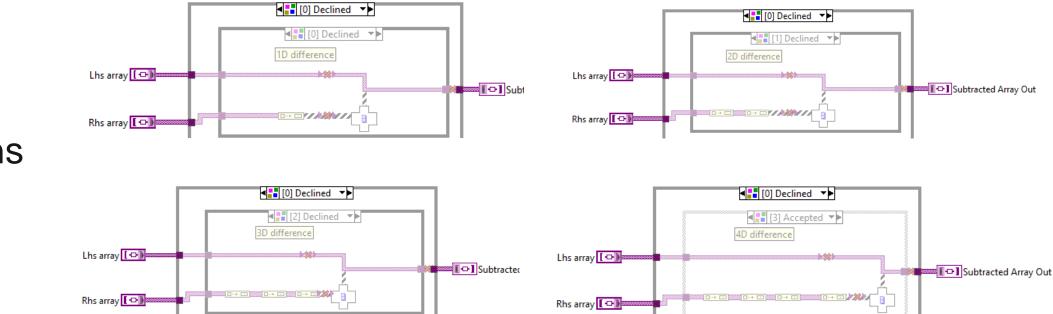




# Maths Array Dimensions

- Not Turing complete
- LabVIEW functions
  - Increment: Build array
  - Decrement: Index array
- Combine for functions
  - Subtract dims





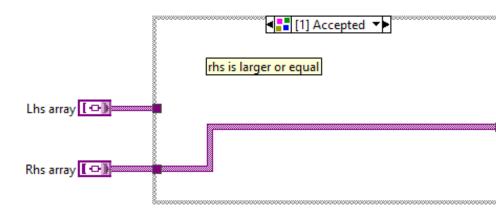
## **Maths** Array Dimensions

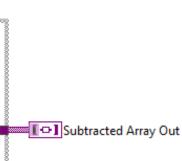
- Not Turing complete -
- LabVIEW functions -
  - Increment: Build array -
  - Decrement: Index array \_
- Combine for functions \_
  - Subtract dims

-

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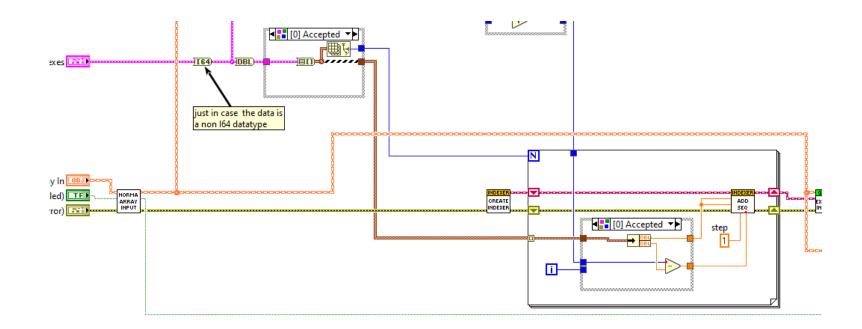
- Search for greatest dimension

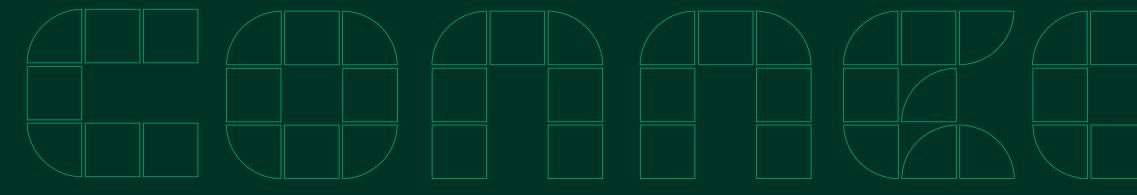




## Maths Clusters

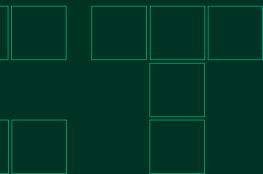
- Values and indexing
- Dynamic structures
- Can be converted to arrays
  - Array of numbers
  - Array of clusters





## Debugging

Convert to VI





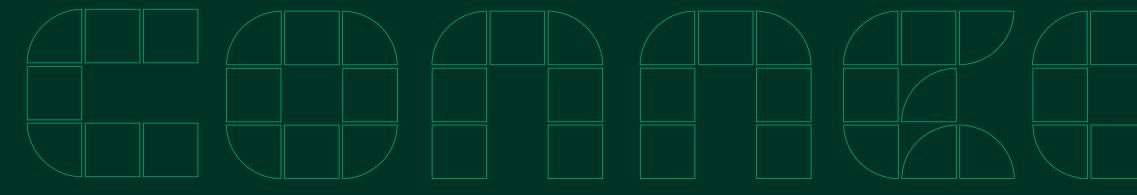
# **Convert to VI**

- Freeze the state of a malleable VI when it's in a calling VI
- Debugging:
  - Find undesired datatype behavior
  - Place probes to find programming issues
- Caution:
  - Changes are not represented in VIM's
  - The function won't dynamically change

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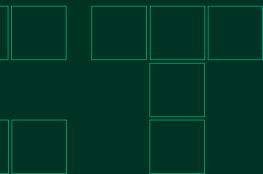


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## Debugging

Project bedrock

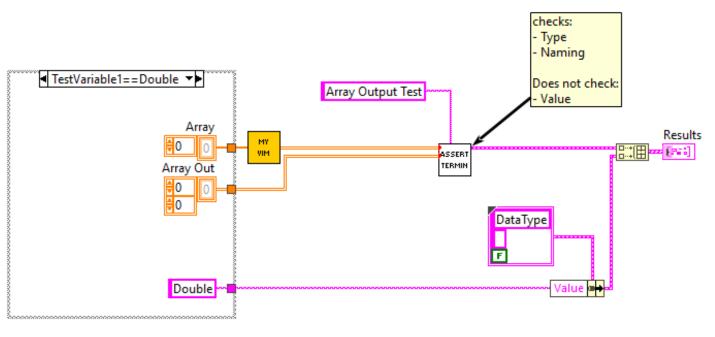


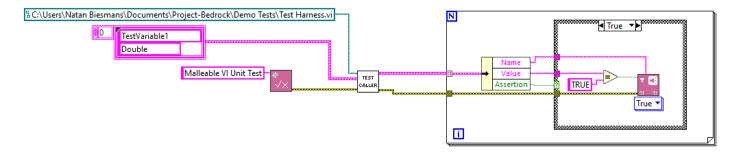


# **Test Driven Design**

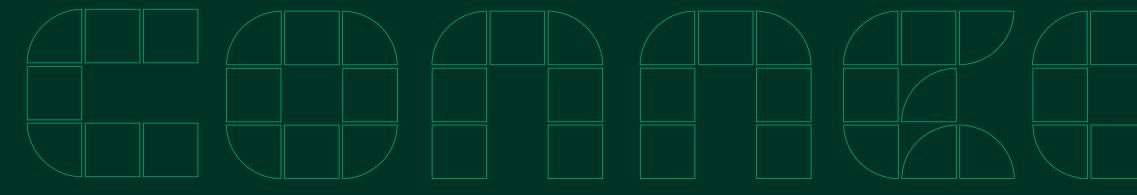
- Test type behavior
- FOSS tool
  - Under development
  - Cycle through states
    - single test harness
    - Multiple structures for vectors
  - JKI Caraya





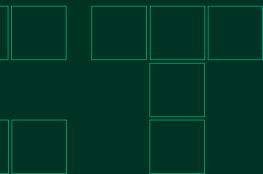


- NI Unit Tests



#### **Malleable VI's Quirks**









# 

LinkedIn

Github

NatanBiesmans

G<sup>2</sup>CPU

VIPM

G2CPU



